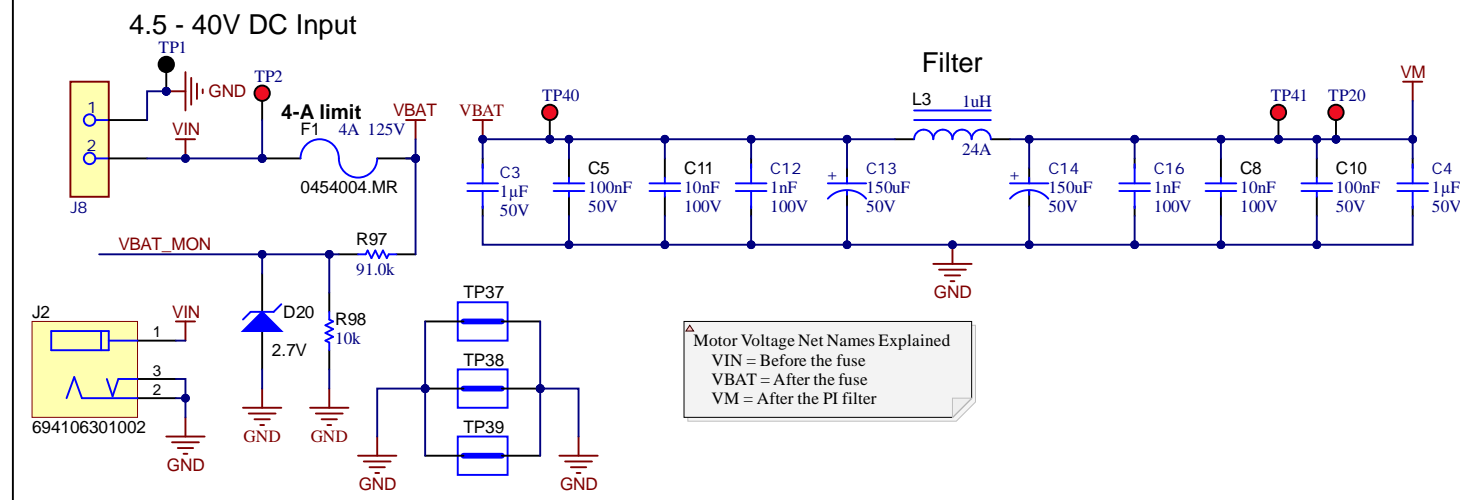
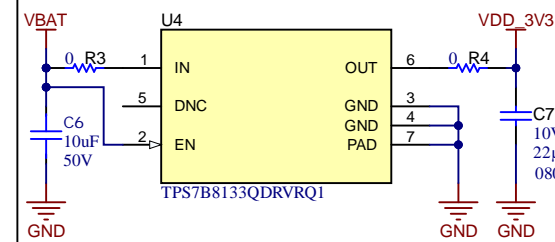


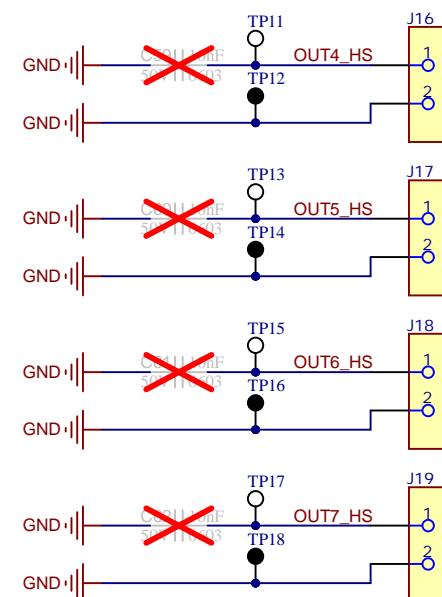
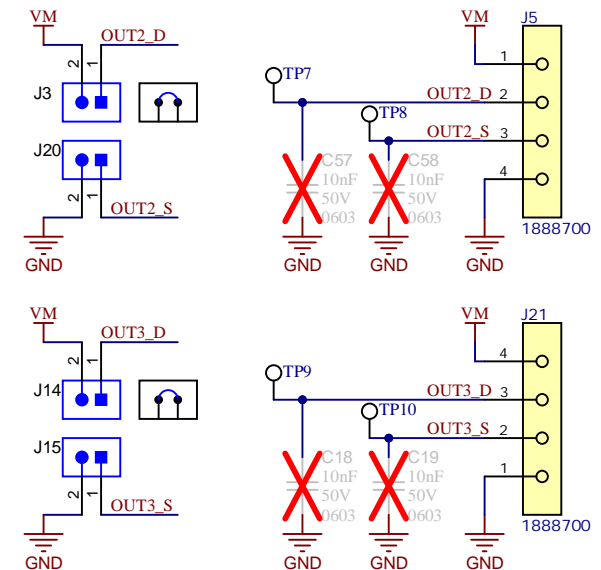
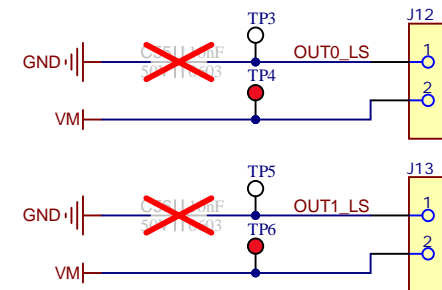
## Board power



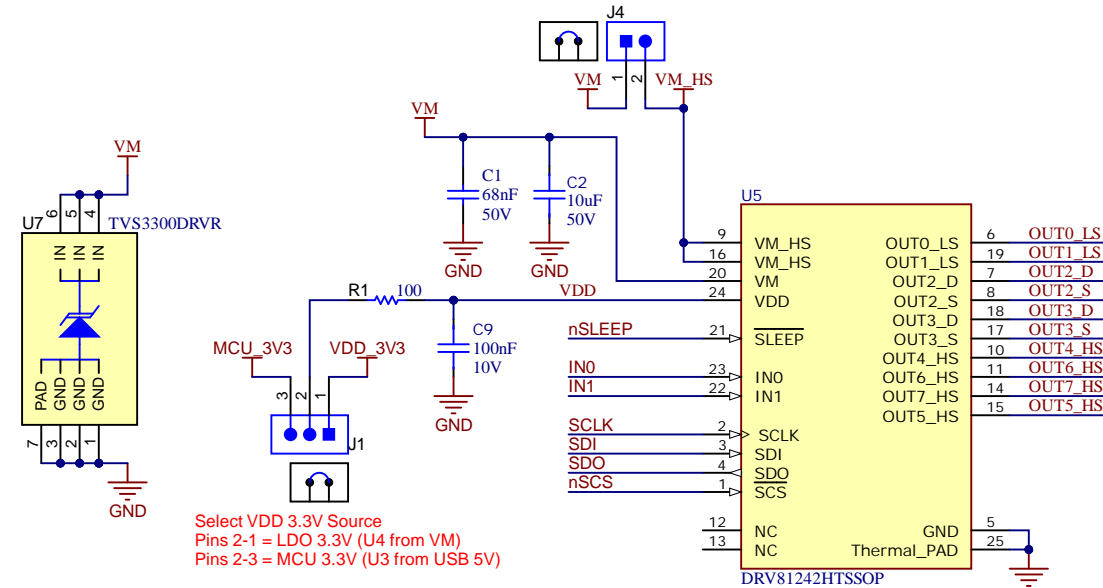
### 3.3V LDC



## Output Connectors

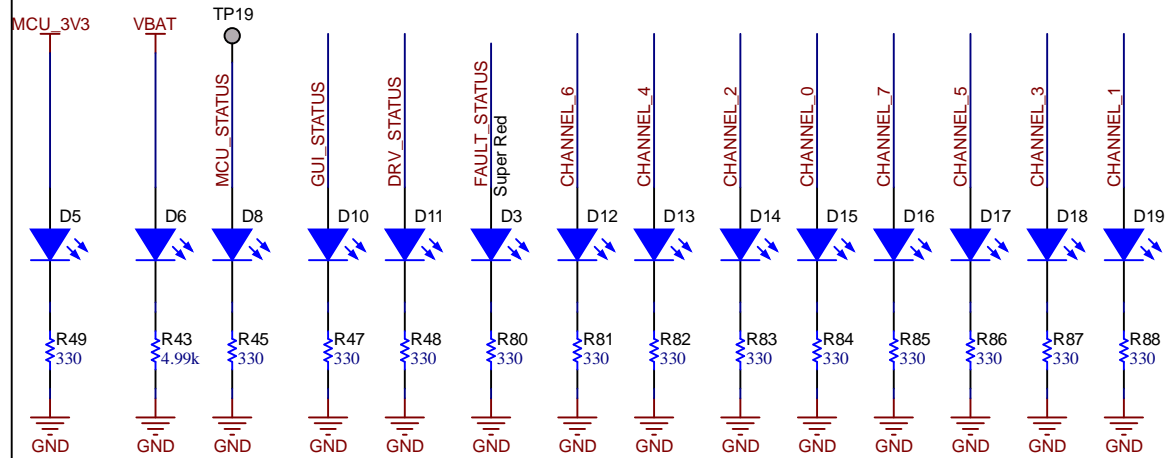


**DRV81242-Q1**

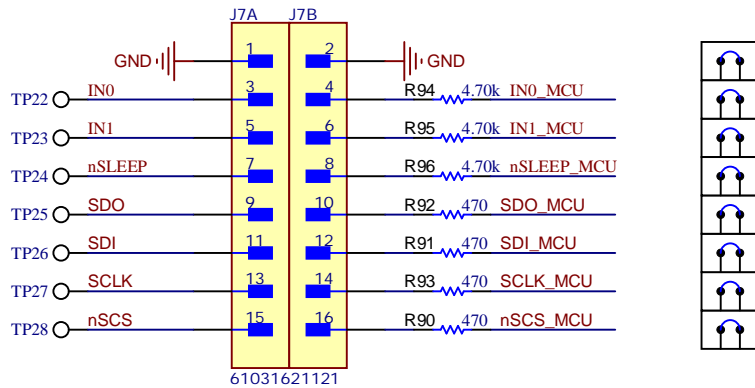


Select VDD 3.3V Source  
Pins 2-1 = LDO 3.3V (U4 from VM)  
Pins 2-3 = MCU 3.3V (U3 from USB 5V)

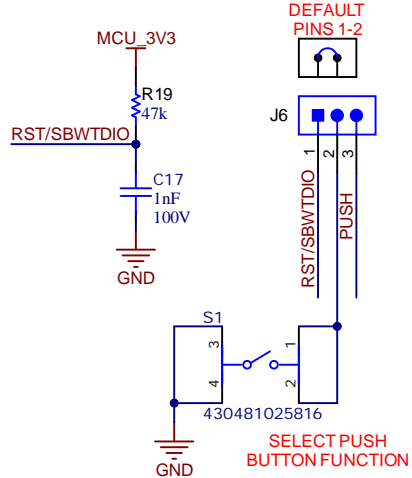
## LEDS



## Main Signal Header

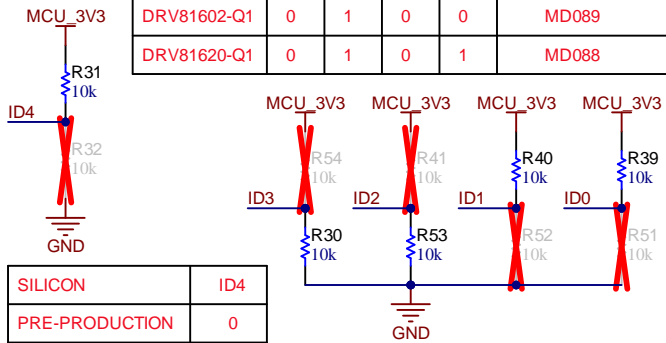


## RST/PUSH Button



## ID Resistors

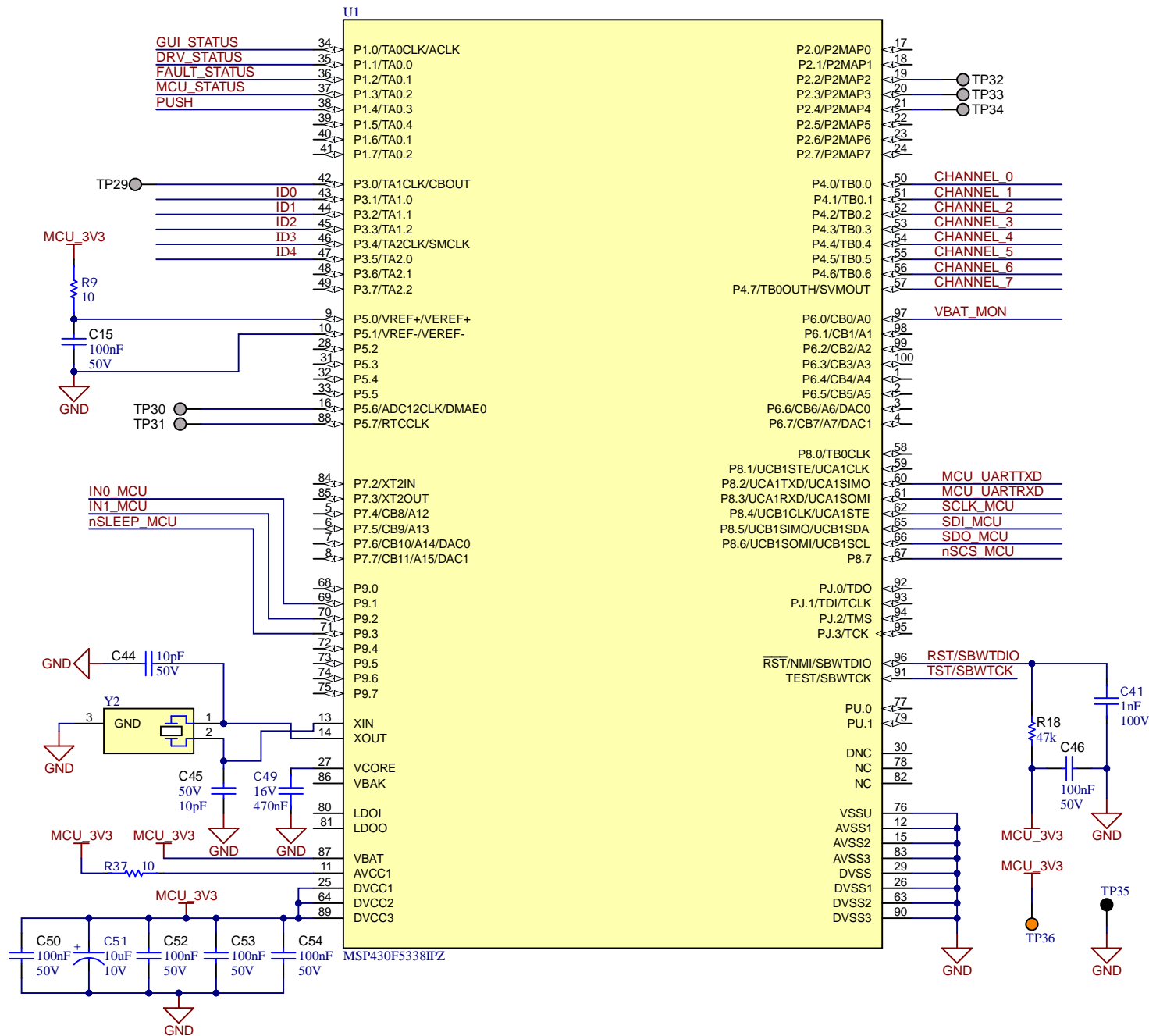
Device	ID3	ID2	ID1	ID0	MD #
DRV81008-Q1	0	0	0	0	MD075
DRV81004-Q1	0	0	0	1	MD086
DRV81080-Q1	0	0	1	0	MD081
DRV81242-Q1	0	0	1	1	MD091
DRV81602-Q1	0	1	0	0	MD089
DRV81620-Q1	0	1	0	1	MD088



SILICON	ID4
PRE-PRODUCTION	0
PRODUCTION	1

The resistors on the ID[3:0] nets inform the firmware which device ID variant is on this board

## MSP430



Orderable: DRV81242-Q1EVM	Designed for: Public Release	Mod. Date: 7/19/2024
TID #: N/A	Project Title: MD091	
Number: MD091	Rev: B	Sheet Title:
SVN Rev: Unknown revision	Assembly Variant: 001	Sheet: 2 of 4
Drawn By: David Medis	File: MD091_MCU_SchDoc	Size: B
Engineer: David Medis	Contact: http://www.ti.com/support	



